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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/726,337	12/01/2000	Hongyong Zhang	0756-2228	1080

7590 04/24/2002

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EXAMINER

COLEMAN, WILLIAM D

ART UNIT	PAPER NUMBER
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2823

DATE MAILED: 04/24/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/726,337

Applicant(s)

ZHANG ET AL.

Examiner

W. David Coleman

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 11 March 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 2-23 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 2-23 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 6,7,11-1
- 4) ☐ Interview Summary (PTO-413) Paper No(s) \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

2. The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claims 2, 3, 4, 5, 6, 7, 8, 10, 12, 15, 18 and 21 are rejected under 35 U.S.C. 102(e) as being anticipated by Yonehara EP 0 390 608 A2.

4. Pertaining to claims 2 and 3, Yonehara discloses a semiconductor device as claimed. See FIG. 17, where Yonehara (302) teaches a semiconductor device comprising:

a source region and a drain region;

a channel formation region provided between said source region and said drain region and provided in a crystalline semiconductor comprising silicon;

wherein said channel formation region contains an element selected from group IV elements other than silicon (page 4, line 11, i.e., Sn), and

wherein said channel formation region comprises a plurality of crystal provided in said crystalline semiconductor and extending in a same direction.

5. Pertaining to claims 4, 5, 6 and 7, Yonehara teaches a semiconductor device comprising: a source region and a drain region (not numbered, however all thin film transistors inherently have source drain and channel formation regions);

a channel formation region provided between said source region and said drain region provided in a crystalline semiconductor comprising silicon;

wherein said channel formation region contains an element selected from group IV elements other than silicon, and

wherein said source region, said drain region and said channel formation region are arranged in parallel with a plane,

wherein said channel formation region comprises a plurality of crystals provided in said crystalline semiconductor,

wherein said plurality of crystals are in parallel with said plane in parallel with which said source region, said drain region and said channel formation region are arranged, and wherein concentration of said element in said channel formation region is  $5 \times 10^{19}$  atoms/cm<sup>3</sup> or less. Please note that Yonehara teaches wherein an intersecting angle between said same direction and a direction connecting said source region and said drain region is adjusted in order to control resistance against movement of carriers in said channel formation region.

6. Pertaining to claims 8, 10, 12, 15, 18 and 21, Yonehara teaches wherein said element is selected from the group consisting of Sn.

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7. Claims 9, 11, 14, 16, 17, 20 and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yonehara, EP Patent Application EP 0 390 608 A2 as applied to claims 2, 3, 4, 5, 6, 7, 8, 10, 12, 15, 18 and 21 above and in further view of Ono et al., U.S. Patent 5,294,560.

8. Yonehara discloses a semiconductor device substantially as claimed as discussed above. However, Yonehara fails to teach wherein said semiconductor device is selected from the group consisting of an active matrix type display and an image sensor. Ono teaches a semiconductor device consisting of a active matrix display. See Title, in view of Ono, it would have been obvious to one of ordinary skill in the art to incorporate the active matrix type display of Ono into the Yonehara semiconductor device because they are adapted to serve individual picture elements (column 1, lines 22-30).

9. Claims 13, 16, 16, 19 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yonehara European Patent Application 0 390 608 as applied to claims , 3, 4, 5, 6, 7, 8, 10, 12, 15, 18 and 21 and further in view of Nakagiri et al., U.S. Patent 4,740,829.

10. Yonehara discloses a semiconductor device substantially as claimed as discussed above. However, Yonehara fails to teach wherein said concentration of said element is measured by Secondary Ion Mass Spectroscopy. Nakagiri teaches a semiconductor device wherein Secondary Ion Mass Spectroscopy measures a concentration of an element (column 4, lines 42-44). In view of Nakagiri, it would have been obvious to one of ordinary skill in the art to incorporate SIMS into the Yonehara semiconductor device because it is an analytical method to calculate the atomic percentage of the material in question (column 4, line 46).

### ***Double Patenting***

11. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or

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improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

12. A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

13. Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

14. Claims 4-7 and 12-23 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-26 of U.S. Patent No. 6,160,279.

Although the conflicting claims are not identical, they are not patentably distinct from each other because it is well known that a formation region is a functional requirement for thin film transistor.

### ***Conclusion***

15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to W. David Coleman whose telephone number is 703-305-0004. The examiner can normally be reached on 9:00 AM-5:00 PM.

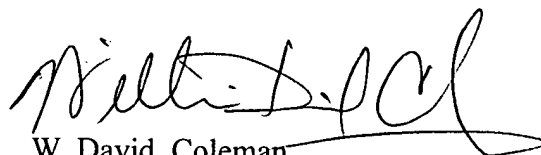
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael M. Fahmy can be reached on 703-308-4918. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7721 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

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A handwritten signature in black ink, appearing to read "W. David Coleman". The signature is fluid and cursive, with a long horizontal stroke extending to the right.

W. David Coleman

Examiner

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WDC

April 17, 2002